

<b>Notice of References Cited</b>	Application/Control No. 10/719,341		Applicant(s)/Patent Under Reexamination LYSAGHT, PATRICK	
	Examiner Phallaka Kik		Art Unit 2825	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,189,628 A	02-1993	Olsen et al.	716/7
*	B	US-5,452,467 A	09-1995	May et al.	716/1
*	C	US-5,784,291 A	07-1998	Chen et al.	716/10
*	D	US-6,047,127 A	04-2000	McCarten et al.	717/173
*	E	US-6,446,242 B1	09-2002	Lien et al.	716/6
*	F	US-6,502,189 B1	12-2002	Westby, Judy Lynn	713/1
*	G	US-6,496,971 B1	12-2002	Lesea et al.	716/16
*	H	US-2003/0208723 A1	11-2003	Killian et al.	716/1
*	I	US-2004/0158699 A1	08-2004	Rhoads et al.	713/001
*	J	US-2004/0177240 A1	09-2004	Yang, Pachinco	713/001
*	K	US-2005/0108662 A1	05-2005	Morfey et al.	716/001
*	L	US-7,028,281 B1	04-2006	Agrawal et al.	716/12
*	M	US-2006/0259878 A1	11-2006	Killian et al.	716/001

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	EP 858167 A1	08-1998	European Patent	MARSHALL et al.	H03K 19/177
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Selvaraj et al., "FSM Implementation in Embedded Memory Blocks of Programmable Logic Devices Usign Functional Decomposition", Proceedings of International Conference on Information Technology: Coding and Computing, 8-10 April 2002, pp. 355-360.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.